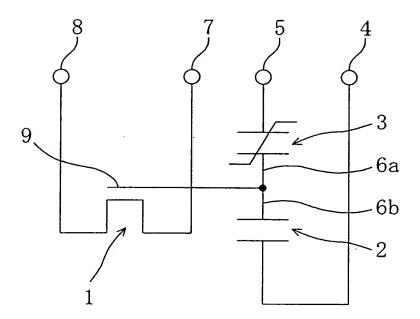
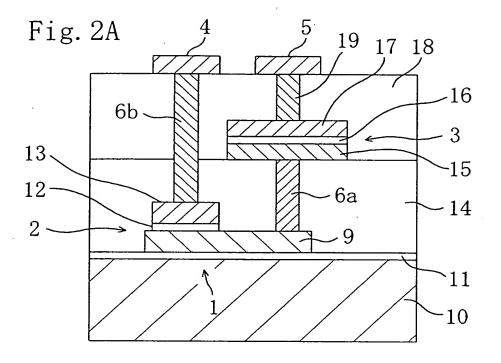
Fig. 1





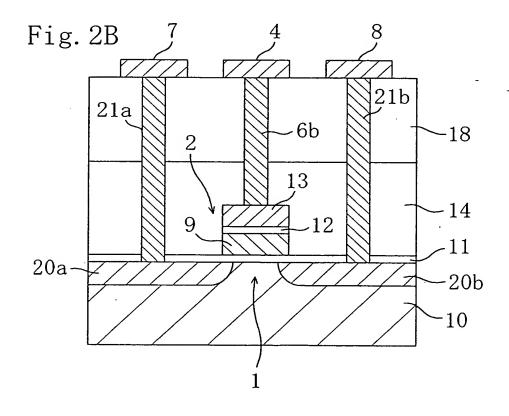


Fig. 3

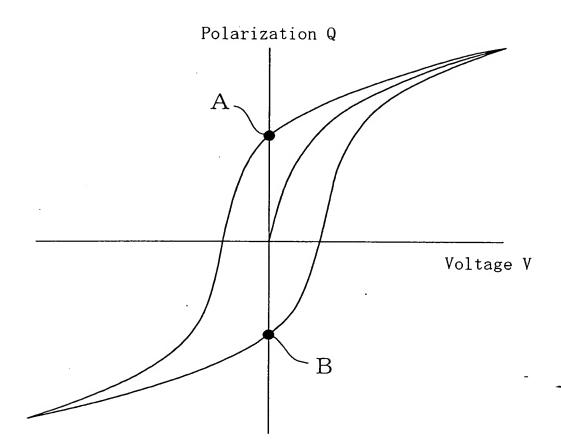


Fig. 4

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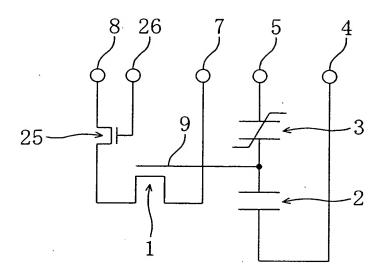


Fig. 5

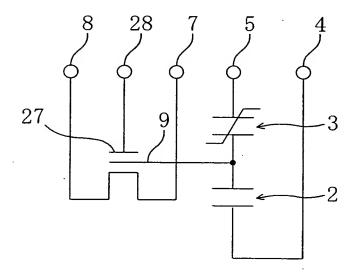
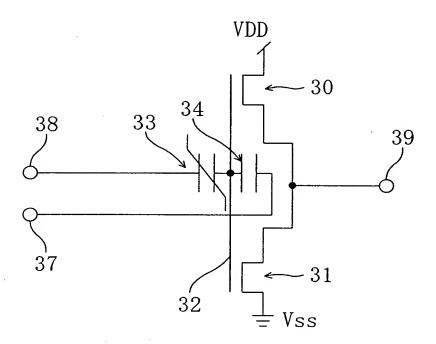
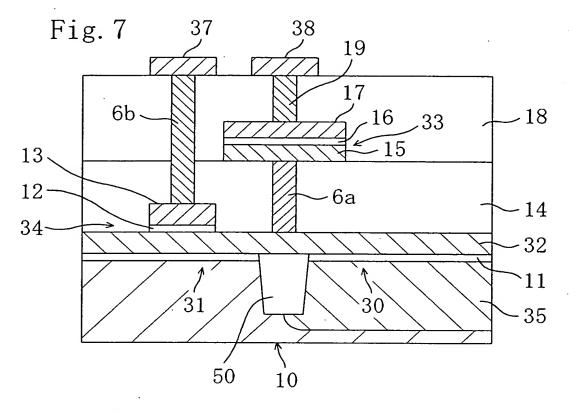
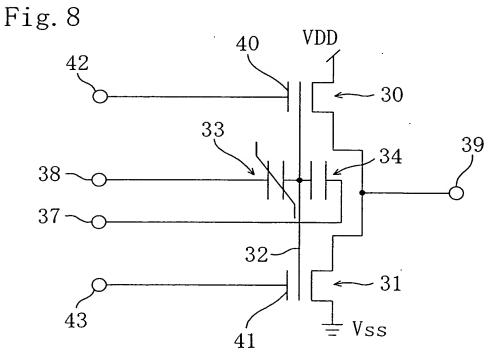
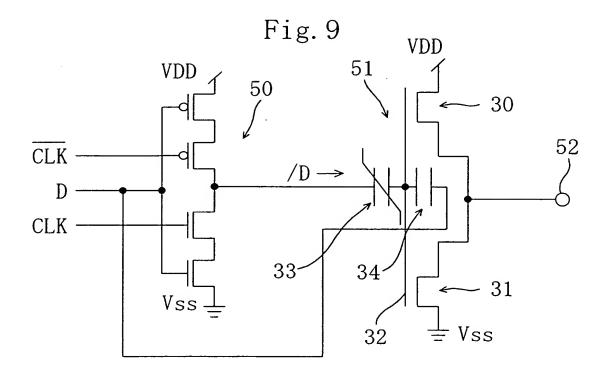


Fig. 6









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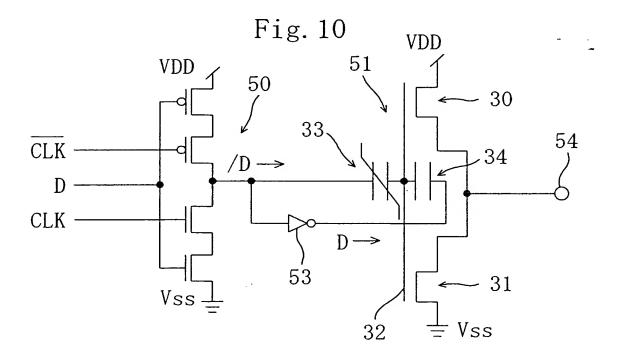


Fig. 11

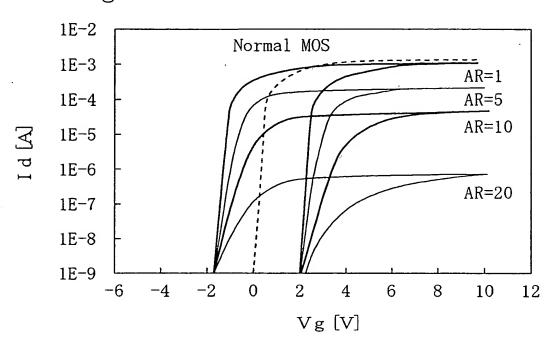


Fig. 12
PRIOR ART

pMISFET

nMISFET

VDD

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Note: The second containing the second c

Fig. 13 PRIOR ART

